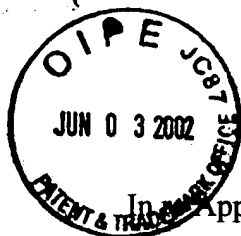


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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Application of: JEFFREY W. SCOTT, ET AL.

Filed: February 22, 2002

For: CALIBRATED LOW-NOISE CURRENT AND VOLTAGE
REFERENCES AND ASSOCIATED METHODS

Serial No.: 10/081,121 ✓

Group Art Unit: UNKNOWN

Examiner: UNKNOWN

Atty Docket No.: SILA:095

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Assistant Commissioner For Patents
Washington, D.C. 20231

PRELIMINARY AMENDMENT

Please amend the application as follows.

In the specification:

The rewritten clean versions of all the specification changes are provided below. Attached at the end of this paper is an Appendix providing an indication of the changes relative to the prior version of the specification, as now required by Rule 121.

Please replace the paragraph beginning on page 1, line 27 and ending on page 2, line 17 with the following:

Furthermore, this patent application incorporates by reference the following patent documents: U.S. Patent Application Serial No. 10/075,122, Attorney Docket No. SILA:078, titled "Digital Architecture for Radio-Frequency Apparatus and Associated

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